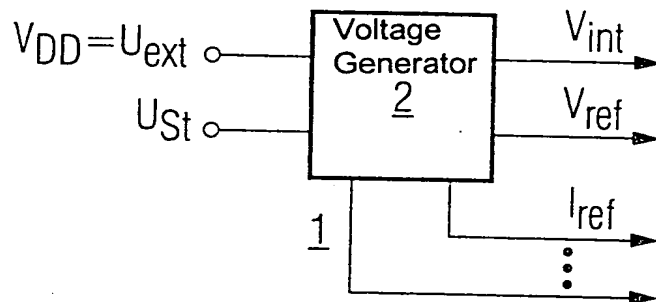
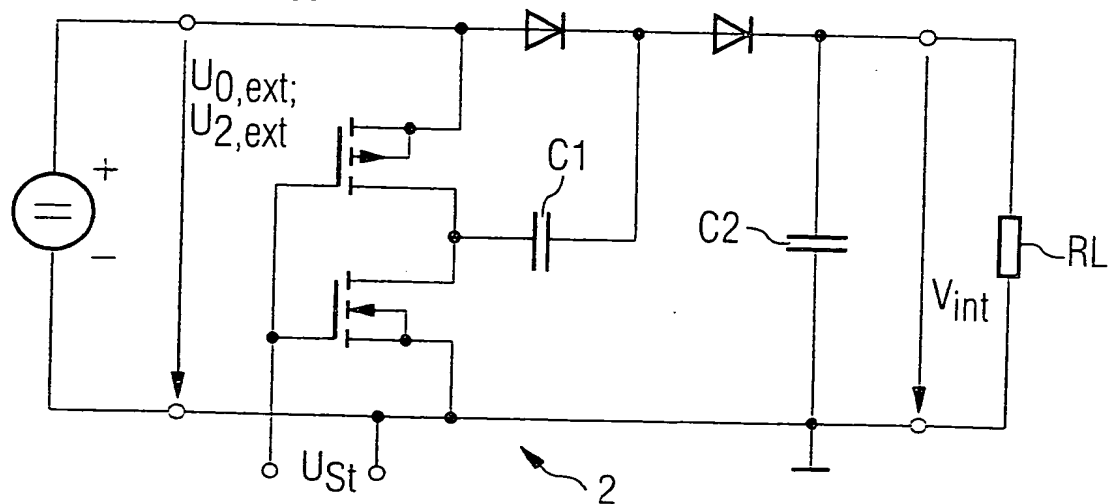
**FIG 2****FIG 3**  
**PRIOR ART**

FIG 4A

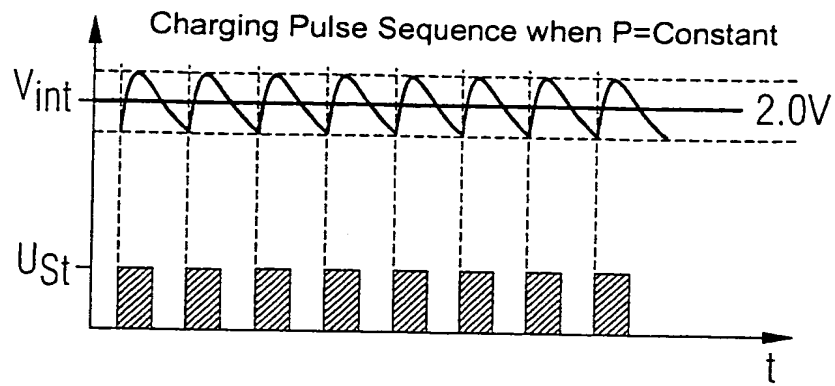


FIG 4B

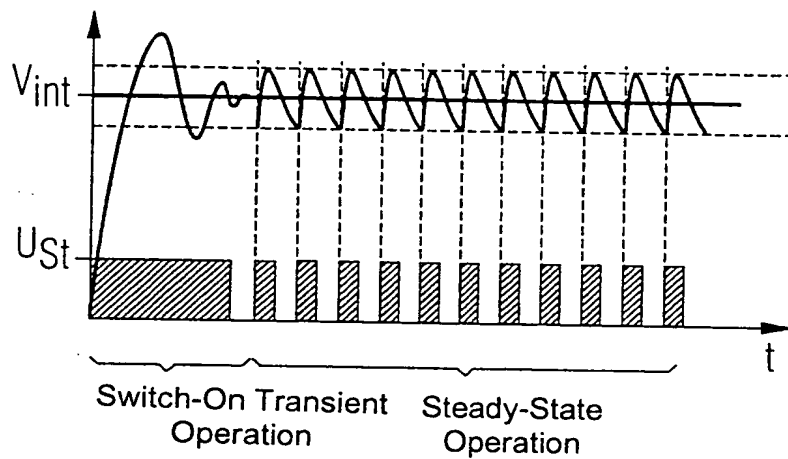


FIG 4C

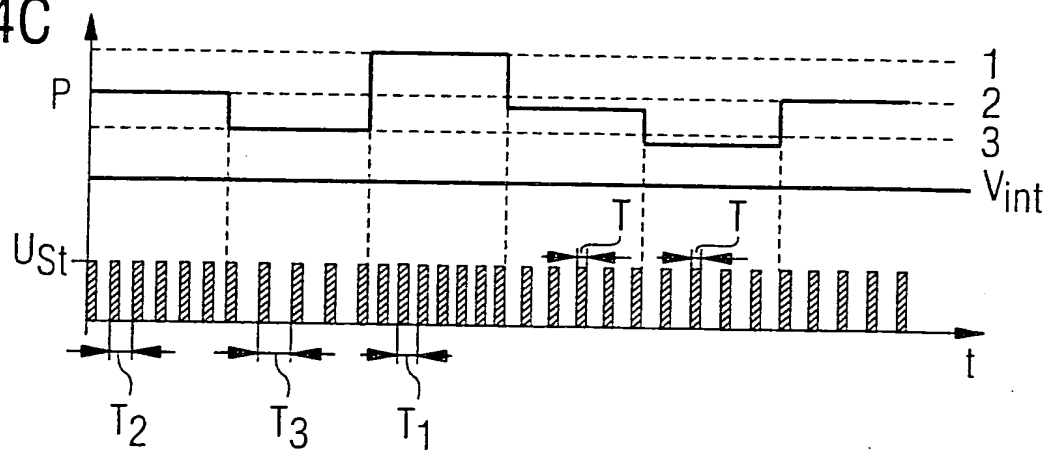


FIG 4D

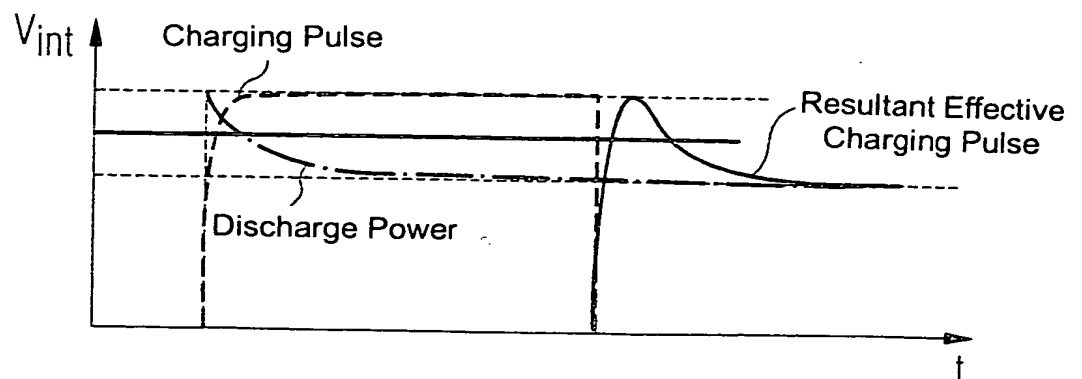


FIG 5

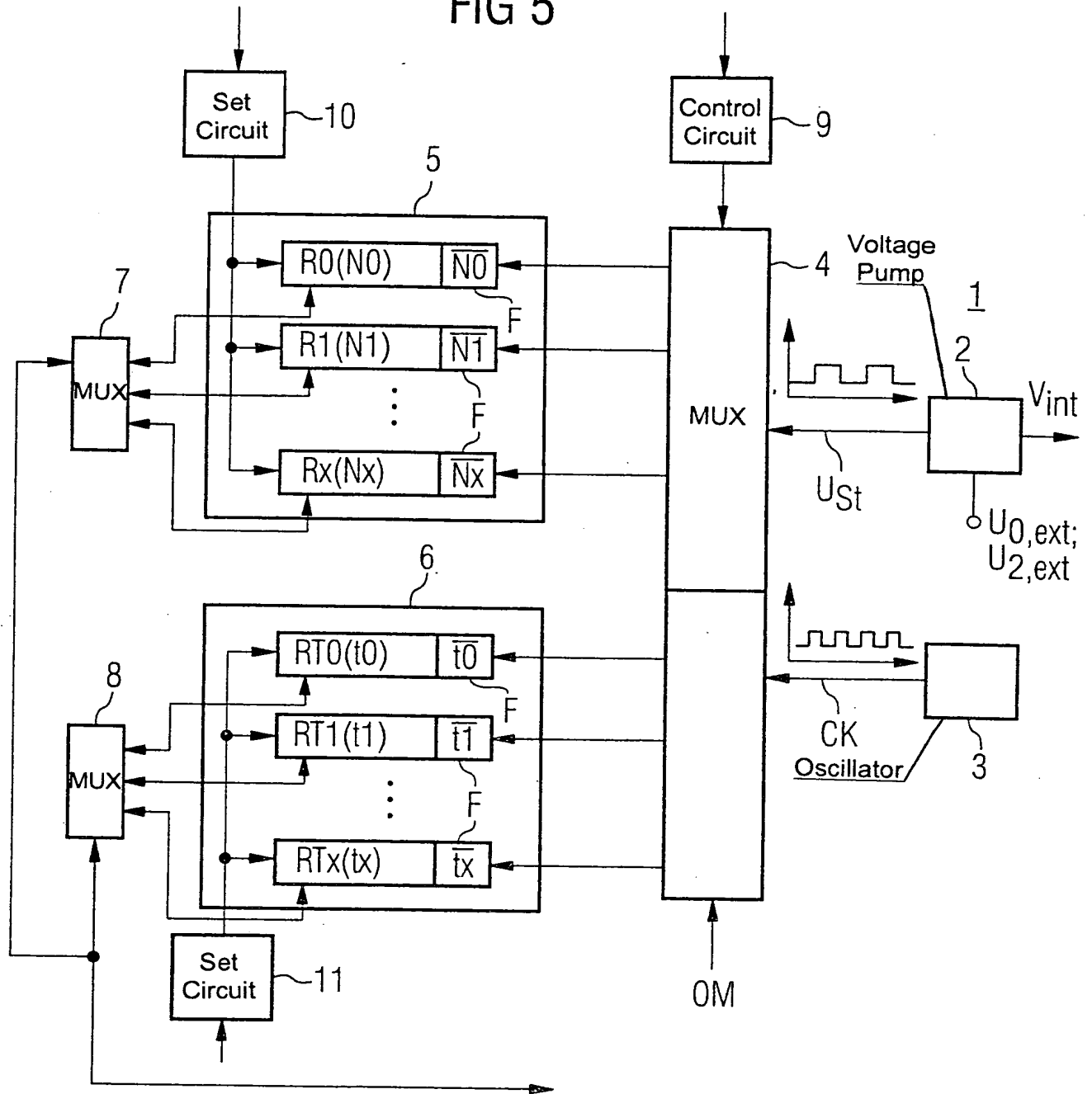


FIG 6

## Reference Operating Mode

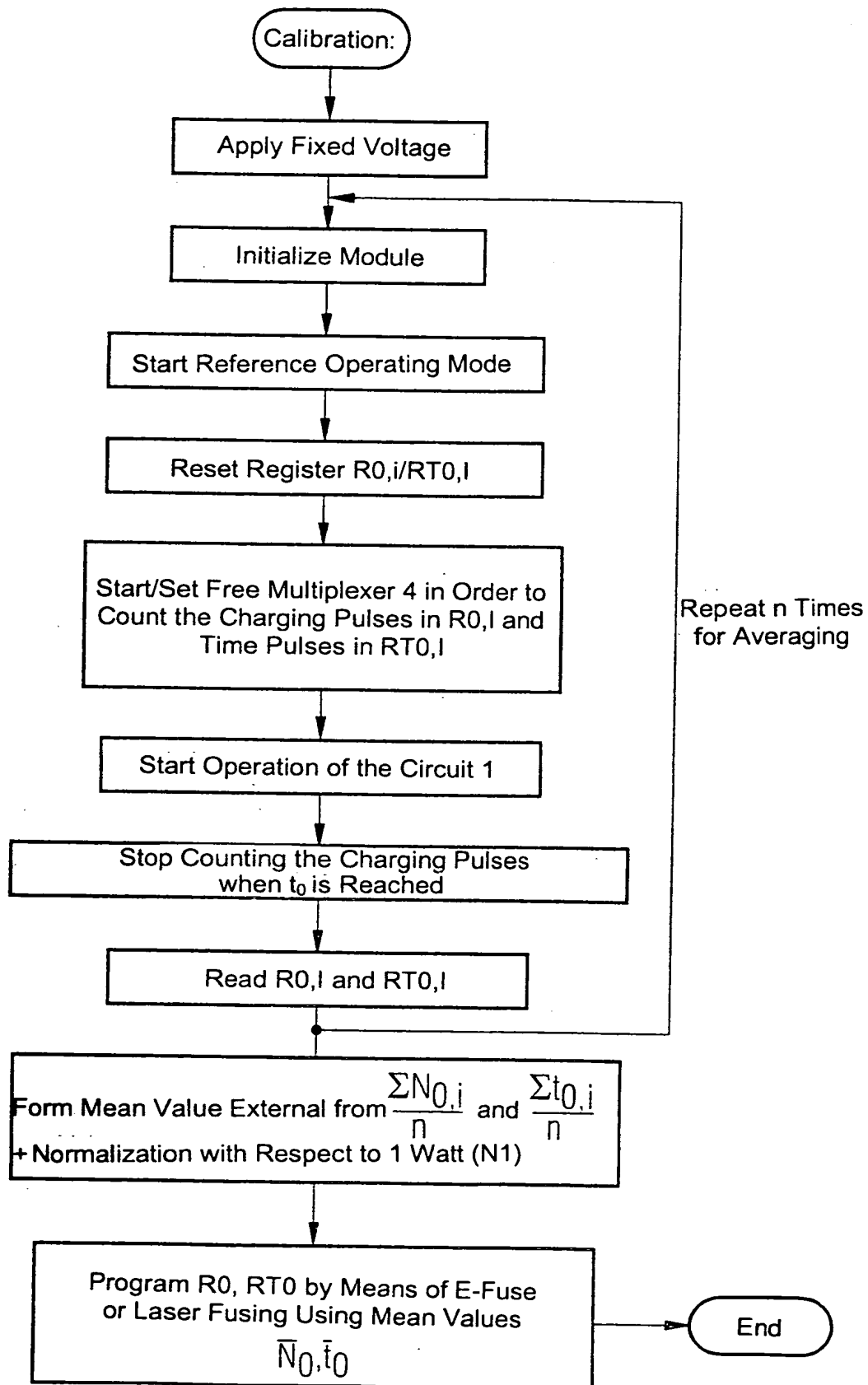


FIG 7

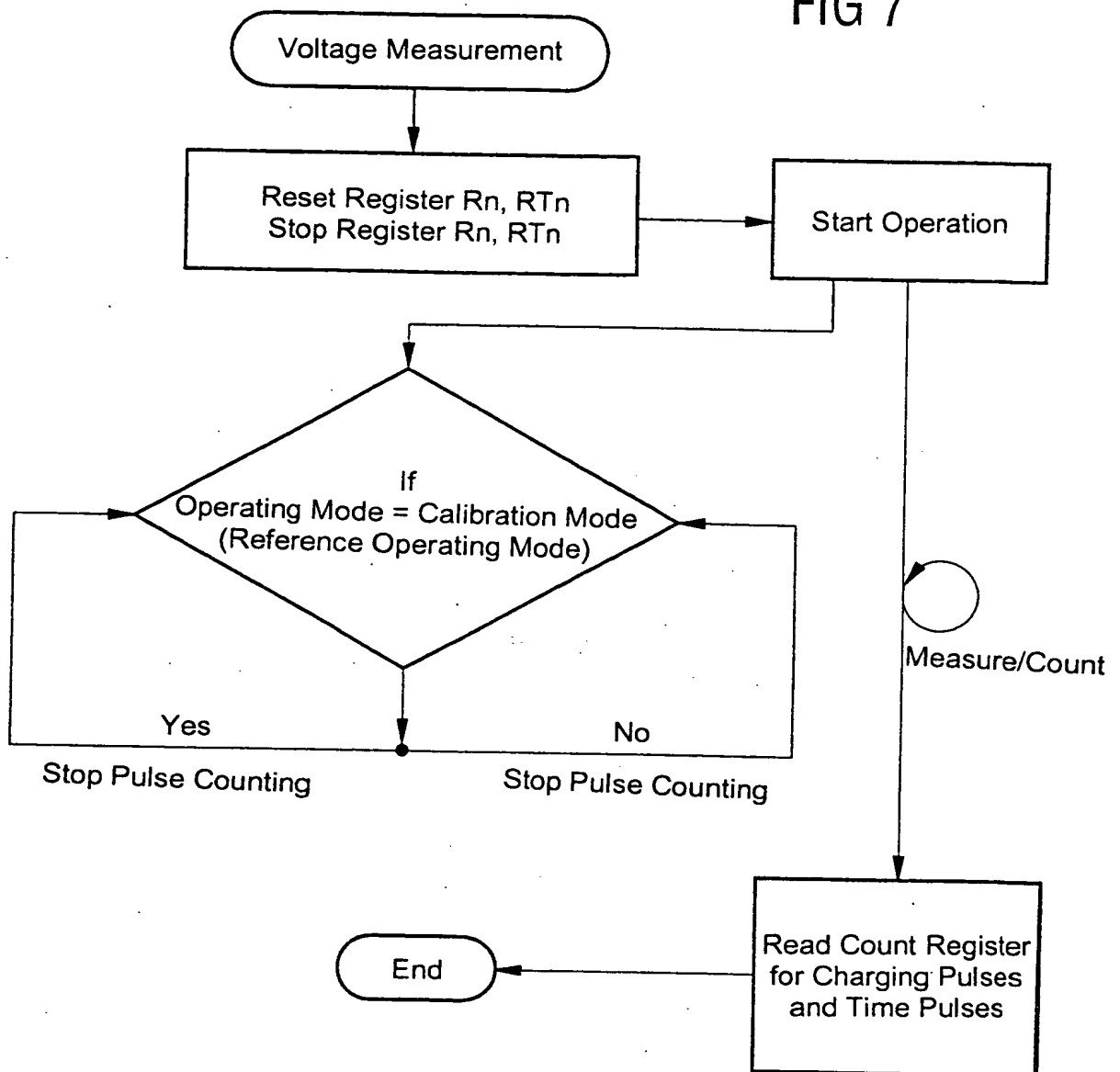


FIG 8

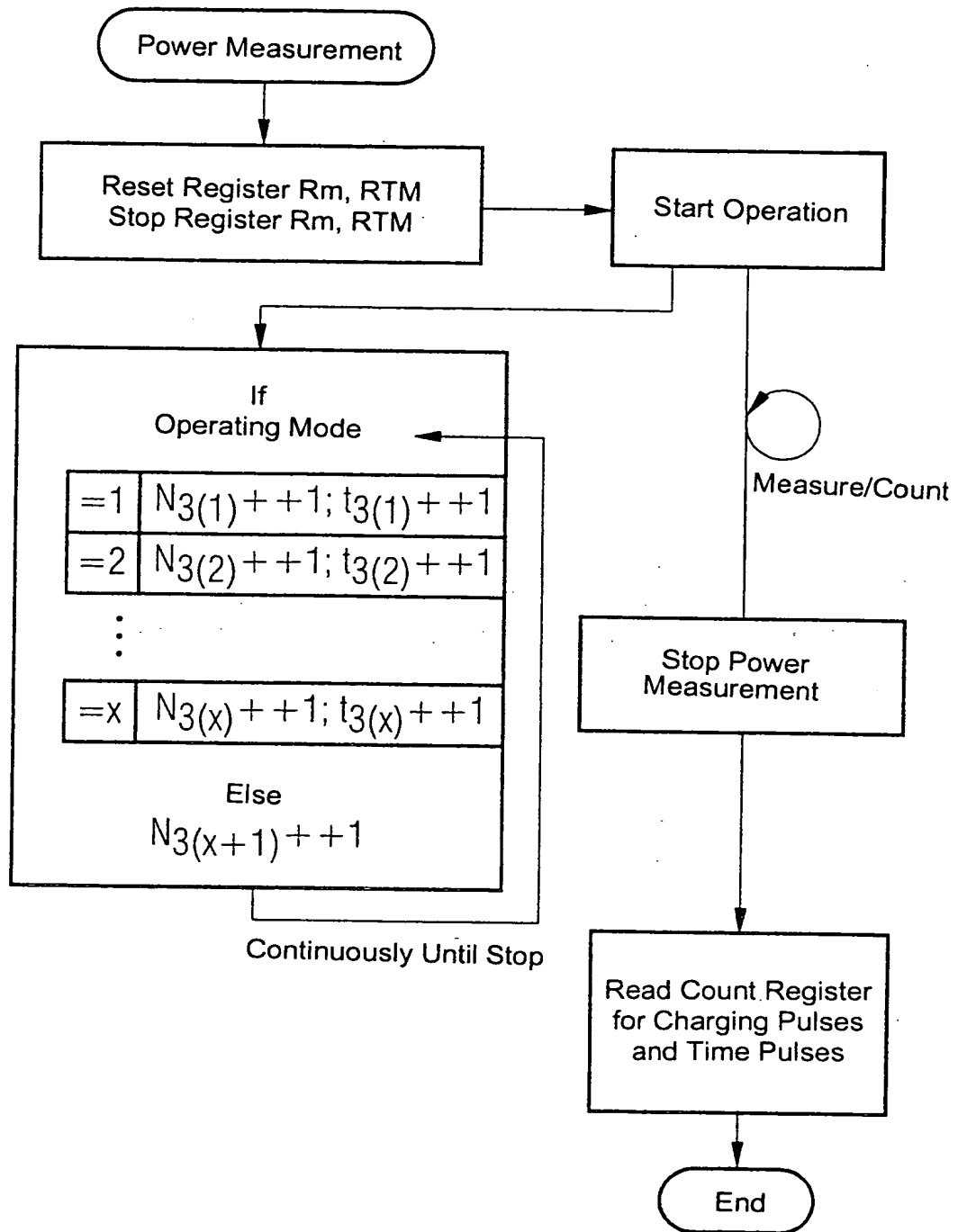


FIG 9

